Serial No. 99/274,935  March 23, 1999  Alexander Markoff  TO THE ASSISTANT COMMISSIONER FOR PATENTS:  Transmitted herewith is an amendment in the above-identified application.  The fee has been calculated and is transmitted as shown below.  CLAIMS AS AMENDED  CLAIMS REMAINING HIGHEST # NUMBER EXTRA RATE FEE  ADDITIONAL FEE  O X \$18.00 \$0.00  Multiple Dependent Claims (check if applicable)	AMENDMENT TRANSMITTAL LETTER (Large Entity)  Applicant(s): Kathleen L. Covert et al.							Docket No. EN997064	
TO THE ASSISTANT COMMISSIONER FOR PATENTS:  Transmitted herewith is an amendment in the above-identified application.  The fee has been calculated and is transmitted as shown below.  CLAIMS AS AMENDED  CLAIMS REMAINING HIGHEST # NUMBER EXTRA RATE FEE  TOTAL CLAIMS 20 20 = 0 x \$18.00 \$0.00  INDEP. CLAIMS 4 - 4 = 0 x \$80.00 \$0.00  Multiple Dependent Claims (check if applicable)	Jg					off	· ·		
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Multiple Dependent Claims (check if applicable)  TOTAL ADDITIONAL FEE FOR THIS AMENDMENT  No additional fee is required for amendment.  Please charge Deposit Account No. in the amount of A duplicate copy of this sheet is enclosed.  A check in the amount of to cover the filing fee is enclosed.  The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 09-0457 A duplicate copy of this sheet is enclosed.  Any additional filing lees required under 37 C.F.R. 1.16.  Any patent application processing fees under 37 CFR 1.17.  Dated:  I certify that this document and fee is being deposited on 3 2 1/0 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissione for Ratents, Washington, D.C. 20231.	TOTAL CLAIMS			20 =			x \$	18.00	<del>                                     </del>
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT    No additional fee is required for amendment.   Please charge Deposit Account No. in the amount of A duplicate copy of this sheet is enclosed.   A check in the amount of to cover the filing fee is enclosed.   The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 09-0457  A duplicate copy of this sheet is enclosed.   Any additional filing fees required under 37 C.F.R. 1.16.   Any patent application processing fees under 37 CFR 1.17.	INDEP. CLAIMS	4	-	4 =		0	x \$	80.00	\$0.00
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cc: Mark Levy	TOTAL ADDITIONAL FEE FOR THIS AMENDMENT								



Disclosure No.

EN896-0258

Patent Attorney

J.R. Pivnichny

Title of Invention (Short & Descriptive)

Method for Microetch Cleaning of Copper Circuits



Functional Manager

**CRETEKOS** 

Evaluator

John Pivnichny

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### Inve .ion Discl sure EN896-0258

Page 1

Receiving Time Receiving Date 16:02:15 11/18/96 **Evaluation Area** 

02

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Moschak, Peter A  Area Code Electronic Address 02 MOSCHAK at ENDVM5		Emp.Serial 267601	Div/Dept. 29/T23G	257-2	Endicott	857-7106
			Manager's Name Voya Markovich		Manager's Electronic Address MARKOVIC at ENDVM5	

Table 1. Critical Dates Information	
Date invention workable:	10/01/96
Used or Planned for product:	N
If so, Product Name?	
Release?	
Announce Date?	
Public Demonstration or Use:	N ·
If so, When?	
Where?	
Disclosed to Non-IBMers:	N
If so, When?	
Where?	
CDA in place?	
Use in Manufacturing:	N
If so, When?	·
Where?	
Product Name?	



Method for Microetch Cleaning of Cg

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Inv nti n Disclosure

Page 2 of 3

#### **Problem**

Conventional microetch cleaning of copper circuits (ie. printed circuit cards and boards) in the presence of nickel / gold, or other precious metal plated contact tabs can lead to complete etch out or near etch out of circuit lines due to the galvanic etch effect associated with common etchants in the presence of precious metals.

An example of this occurs on printed circuit boards having gold plated edge connectors. These gold plated fingers are typically connected by 0.006" wide copper traces to the rest of the circuit board. Prior to shipping the finished circuit board, the board is processed thru an "Entek" process consisting of degreaser, sodium persulfate microetch and Entek to prepare the Cu lands for SMT assembly. Boards have frequently been received from vendors after this "Entek" process, in which the Cu circuit lines connecting the gold tabs have been completely, or nearly completely, etched thru due to galvanic etch effects.

Disclosed here is a sodium persulfate microetchant that is free of galvanic etch effects associated with standard microetch sol-

Entek is a trademark of Enthone OMI.

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Solution	· · · · · · · · · · · · · · · · · · ·	0
<ul> <li>A copper microetch solution of the following</li> <li>25-150 grams/liter sodium persulfate</li> <li>0-5% by volume phosphoric acid</li> <li>0-0.25 Molar sodium phosphate dibasic</li> </ul>		ECEIVED IPR-6 2001 700 MAIL ROC
A preferred formulation of this microetal call		3

A preferred formulation of this microetch solution is 75-100 grams per liter of sodium persulfate, 3% by volume phosphoric acid

This etchant is substituted for the standard sodium persulfate microetch chemistry is the standard Entek process flow. Since this formulation is free of galvanic etch effects, circuit boards can be cleaned multiple times without detrimental effects to the copper

Some alternative formulations to the above formulation are as follows:

- · Ammonium, potassium or other persulfates could be substituted for sodium persulfate.
- · Sulfuric acid, or other weak acids could be substituted for phosphoric acid. In the case of sulfuric acid, volume percent should be less than 3%, and preferably less than 1%.
- · Many other phosphate salts could be substituted in place of sodium phosphate dibasic. These could include, by way of example, sodium or potassium phosphates in monobasic, dibasic or tribasic formulations.
- Typical surfactants could be added to the formulation.

## **Evaluation Questions**

# If this problem has been solved before, how was it solved?

One previous solution was to insure that the soldermask or protective coating is applied over the interface of the gold plated teb to the copper circuit line. This prevents copper galvanic etching.

### Why is your solution better?

This solution does not require a change to the circuit board design. Additionally, when the soldermask is brought closer to the gold plated connector, there is a higher probability of causing a plugging problem, due to flaking or abraded soldermask.